

L Number	Hits	Search Text	DB	Time stamp
1	2181	(computer mother adj board memory DRAM\$1 SRAM\$1 ROM\$1) same GHz	USPAT; US-PGPUB; EPO; JPO	2003/10/18 09:29
2	495	((computer mother adj board memory DRAM\$1 SRAM\$1 ROM\$1) same GHz) and (signal\$1 power ground) adj5 (line\$1 wire\$1 wiring\$1 bus\$2) same (interconnect\$4 connect\$4)	USPAT; US-PGPUB; EPO; JPO	2003/10/18 09:32
3	45	((computer mother adj board memory DRAM\$1 SRAM\$1 ROM\$1) same GHz) and (signal\$1 power ground) adj5 (line\$1 wire\$1 wiring\$1 bus\$2) same (interconnect\$4 connect\$4) ) and coaxial near7 (line\$1 wire\$1 wiring\$1 bus\$2) same (interconnect\$4 connect\$4)	USPAT; US-PGPUB; EPO; JPO	2003/10/18 09:33